JSPM Group of Institutes, Pune

Bhagwant Institute of Technology, Barshi
AARAMBH-2K17

ELECTRONICS (E&TC/ELECTRICAL/BIO MEDICAL)

PAPER PRESENTATION
TOPICS

1. Image & Video Processing
2. VLSI Design & Fabrication
3. Radio & Satellite Communication
4. 3G & 4G Technology
5. Embedded System
6. Optical Communication
7. System on Chip (SoC)
8. Ad-hoc Network
9. Speech Processing
   FOR RULES SEE THE LAST PAGE

PROJECT EXHIBITION

1. Electronics Engineering projects by students of SE/TE/BE students.
   2. Not necessarily final year projects.
   3. Projects in any of the sub-branches of Electronics Engineering.
   4. Software based projects are also allowed.
2. Bring one hard copy of synopsis and Send synopsis to mail id
3. aarambhtechfest@gmail.com on or before 20th feb 2016 Synopsis should contain the details regarding group.
4. Project demonstration is must. (At least partial fulfillment of project objective)
5. Requirements for project display should be clearly mentioned.

MICRO PROCESSOR & CONTROLLER PROGRAMMING:-

1. μ TRICS
   Maximum two participants per group.
   Entry fees per group: 200 Rs.
   College I-Card is compulsory.
    1. This contest is based on
        a) Microprocessor b) Microcontroller
    2. On the spot entries are available.
    3. Round First: Elimination Round -Aptitude Test on 8051 microcontroller and 8085

       microprocessor.
    4. Time Duration: 30 min

	5. Round Second: 8051/8085 programming on Keil µvision-3 or 4.

	CIRCUIT SUDOKU 1. Competition will be conducted in two rounds. 2. There will be two members per team. 3. Names of the participants entered in the registration forms shall be considered final and no changes will be allowed thereafter. **Round 1: (Elimination Round)** **1.** 1.This round consists of MCQ type questions. 2.There will be 30 questions based on Electronics fundamentals and design ability. Time Duration:30minutes. **Round 2: (For those who will qualify ROUND 1)** **1.** 1.Problem statement based on circuit design for an application will be given. 2.Proper calculations and circuit diagram is required on paper. 3.Resources such as components, breadboard etc will be provided to implement the circuit built. 4.Use of calculator is allowed. 5.Once the time limit is over, no team will be given chance to complete their circuit, if any found will be disqualified. 6.There will be no access given to the internet or book for reference. 7.No negative marking. 8.In case of tie judges and organizers have the right to take the final decision. 9.Time Duration:1 hrs. **2.**

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CIRCUIT DESIGN

3. 1 CIRCUIT DESIGN:
Maximum two participants per group.
Entry fees per group: 200 Rs.
College I-Card is compulsory.
1. First round will be elimination round with objective type questions from the fields of Electronics. (For T.E. and B.E. students will be Expert Technical Aptitude Test. For F.E. and S.E. students will be Beginners Aptitude Test).
2. Time limit for each test will be 30 minutes.
3. Second round will be circuit designing round in which actual applications has to be made.
4. You will get question sheets/components on the registration desk and you have to Design given Circuit.
5. Time limit for this round will be 60 minutes.
6. There will be time bonus.
7. Those participants or groups who are getting output as first will be declared as winner.
8. All the components and materials required for the circuit provided by us.
9. Power supply and other testing device like multimeter, CRO (if required). Other tools will be made available in a shared hall.
10. Spot entries are available.

TECH QUIZ

Technical Quiz Rules:-
1. A team shall consist of max two students .
 2. The decision of the quiz-master will be final and will not be subjected to any change.
3. Participants shall not be allowed to use mobile or other electronic instruments.
4. Questions shall be in the form of multiple choice, True / False statement, Visuals, Specific-answer question etc.

Elimination Round :-
1. Each team would be given a set of question paper containing 20 multiple choice objective type questions.
2. Time limit - 20 minutes.
3. The qualifying Teams would be selected for final stage rounds.
4. In case of tie between 2 or more teams, further 5 questions would be asked for final selection.
 5. The selected teams shall have to appear for the final round.

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Final Quiz – Stage Rounds:-

Round 1:
A question will be asked to a team and if they are unable to answer, it will be passed to the next team .
 1. Each team would be asked 2 questions each.(Objective type or specific answer questions)
2. 10 marks for each correct answer.
3. If a team cannot answer the question, they can pass the question & then the question would be forwarded to the next team.
4. Answering time is only 30 seconds.
 5. Team discussion is allowed.

Round 2: Rapid Fire Round: -
Each team will be asked as many questions as possible one after another in one minute of time.
1. 10 marks for the correct answer and negative 5 marks for the wrong answer
2. Team discussion is allowed
3. If a team cannot answer the question, they can say pass for the next question. The question will not be forwarded to the next team.

Round 3 - Visual round –
Teams will be shown clips or Images of components and will have to answer.
1. 10 marks for each correct answer.
2. Each Team would be asked 2 visual questions. (1 slide in each round)
3. Visuals include components and ICs
4. Answering time is only 20 seconds.
 5. No passing to the next team.

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